

DOT1	WS	REP	DOT	WS	REP	DOT	WS	REP	DOT	WS	REP	DOT	WS	REP	DOT	WS	REP	DOT
101	11	40	37	11	40	37	11	40	37	11	40	37	11	40	37	11	40	37
bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit

WS	REP	DOT	WS	REP	DOT	WS	REP	DOT	WS	REP	DOT	WS	REP	DOT	WS	REP	DOT
11	40	37	11	40	37	11	40	37	11	40	37	11	40	37	11	40	37
bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit	bit

.....

FIG. 1

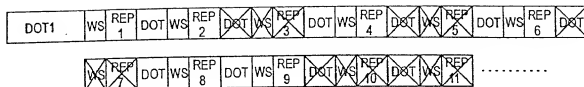


FIG. 2a

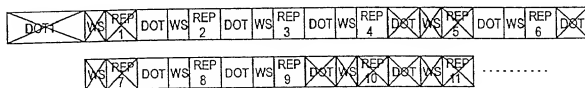


FIG. 2b

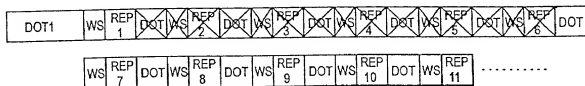


FIG. 2c

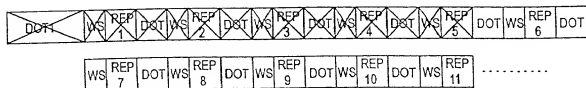


FIG. 2d

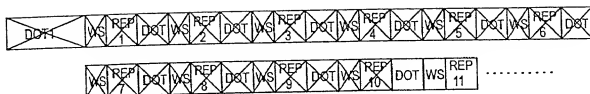


FIG. 3a

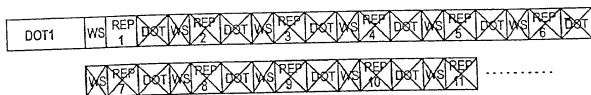


FIG. 3b

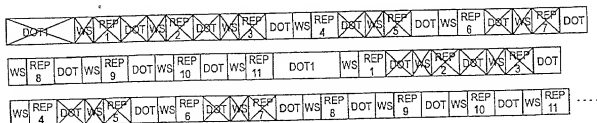


FIG. 4a

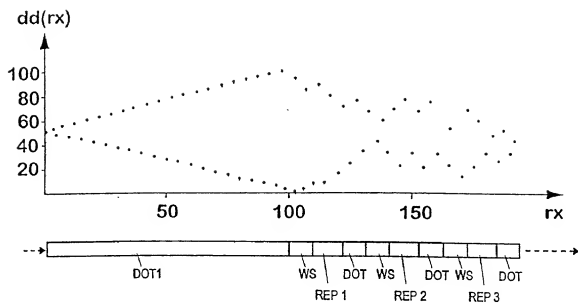


FIG. 4b

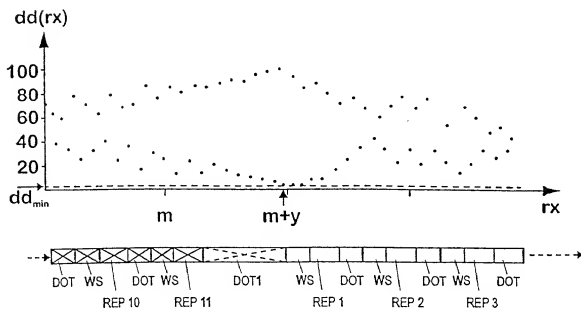


FIG. 4c

